Embedded PLC Hard Logic Solver Instruction Sets

User's Manual

FRECON Electric (Shenzhen) Co., Ltd.

Table of Contents

CHAPTER 1: INTRODUCTION	1
CHAPTER 2: CONTACTS	5
CHAPTER 3: FUNCTION BLOCKS	
T1.0	
T0.1	
T0.01	
UCTR	
DCTR	
ADD	
ADDB	
ADDL	
ADBL	
FADD	
SUB	
SUBB	
SUBL	
SBBL	
FSUB	
MUL	
MULB	
MULM	
MLBM	
MULL	
MLBL	
FMUL	54
DIV	
DIVB	
DIVM	60
DVBM	
DIVL	64
DVBL	
FDIV	
ISQR	
FSQR	72
R->T	74
T->R	76

T->T	
T_CM	
T_SR	
T_RS	
TXHG	
BLKM	
PUSH	
POP	
AND	
OR	
XOR	
COMP	
CMPR	
BROT	
ODSR	
MBIT	
SENS	
DECO	
ENCO	
B->C	
C->B	
SSEG	
PACK	
I->F	
F->I	
JMP	
EOJ	
JSR	
SBR	
RET	
FOR	
NEXT	
HAPTER 4: FLOW CONTROL INSTRUCTIONS	
EOP	

PSUB	156
MOVE	158
RCMP	
CHAPTER 5: SYSTEM RELATED INSTRUCTIONS	
DGET	
DSET	164
DCMP	166
TGET	
TSET	
ТСМР	
STAT	174
CHAPTER 6: OTHERS	177
CAM	177
CDMR	179
CDMW	
PID	

FOREWORD

Embedded PLC series bring the high performance, application flexibility and hardware compatibility to the Embedded PLC family of products. Ten contacts and abundant function blocks (also referred to as instructions) are provided for application control programs using the Embedded PLC series. In this manual, the usage for contacts and function blocks is described together with application examples.

Contact elements include:

- (1) \dashv \vdash (A normally open contact, usually referred to as: "A contact")
- (2) \dashv / \vdash (A normally closed contact, usually referred to as: "B contact")
- (3) -()- (A normal coil)
- (4) -(**S**)- (A set coil)
- (5) -(**R**)- (A reset coil)
- (6) -(\uparrow)- (A positive transitional coil)
- (7) $-(\downarrow)$ (A negative transitional coil)
- (8) -(M)- (A holding coil during power loss)
- (9) -(SM)- (A holding set coil during power loss)
- (10) -(**RM**)- (A holding reset coil during power loss)

Function blocks instructions include:

- Timers and counters: Timers: T1.0, T0.1, T0.01, Counters: UCTR, DCTR.
- (2) Mathematical blocks:

Adders: ADD, ADDB, ADDL, ADBL, FADD, Subtracts: SUB, SUBB, SUBL, SBBL, FSUB, Multipliers: MUL, MULB, MULM, MUBM, MULL, MLBL, FMUL, Dividers: DIV, DIVB, DIVM, DVBM, DIVL, DVBL, FDIV, Square root: ISQR, FSQR.

(3) Register, Table, Array instructions: Move: R->T, T->R, T->T, TXHG, BLKM, PACK, Rotate/Shift: T_RS, BROT, ODSR, Modify: MBIT, Compare: T_CM, CMPR, Search: T_SR, Logic: AND, OR, XOR, COMP Stack: PUSH, POP, Sense: SENS, Encoder, Decoder: ENCO, DECO, SSEG, Convert: B->C, C->B, I->F, F->I.

- (4) Flow control instructions:
 Main program: EOP, SKIP, MCS, MSE, JMP, EOJ,
 Subroutine: JSR, SBR, RET,
 Loop: FOR, NEXT,
 Pointer: INIP, INCP, DECP, PADD, PSUB.
- (5) System related instructions:
 System date: DGET, DSET, DCMP,
 System time: TGET, TSET, TCMP,
 System status: STAT.

(6) Others:

CAM CDMR (CDM read) CDMW (CDM write) MOVE

Users are advised to become familiar with the binary operation (which can be found in any Digital Design Textbook) and the characteristics for each contact element and function block before designing a control application program. Please also be advised that the data and illustrations in this manual are not binding. We reserve the right to modify our products in line with our policy of continuous product improvement. Information in this manual is subject to change without notice and should not be treated as a commitment by FRECON Electric (Shenzhen) Co., Ltd. FRECON assumes no responsibility for any errors that may appear in this manual.

CHAPTER 1: INTRODUCTION

The basic concept required to use this manual and the elements (contacts, function blocks, and instructions) in Embedded PLC is briefly described in this Chapter. In Section 1, the terminology and numerical representation are described. The constituents of a function block are described in Section 2 and the convention used to represent the function blocks is described in Section 3.

SECTION 1: Terminology and Numerical Representations: BIT:

The basic unit of the binary system. The value of a bit is either 0 or 1. The abbreviation for bit is B, such as B0, B1,etc.

NIBBLE:

A nibble is composed of four bits such as B3~B0. It can be used to represent decimal values ranging from 0 to 9, or hexadecimal values ranging from 0~F. The abbreviation for nibble is NB, such as NB0, NB1, etc.

BYTE:

A byte is composed of eight bits (B7~B0) or two contiguous nibbles (NB1~NB0). It can be used to represent hexadecimal values ranging from 00~FF. The abbreviation for byte is BY, such as BY0, BY1, etc.

WORD:

A word is composed of sixteen bits. It can be used to represent hexadecimal values ranging from 0000~FFFF or 0~65535 in the decimal system. The abbreviation for word is W, such as W0, W1, etc. Since Embedded PLC is based on 16-bit microcomputer architecture, a word occupies one register in the computer memory.

LONG WORD:

A long word is composed of two continuous words or 32 bits. It can be used to represent hexadecimal values ranging from 0000000~FFFFFFF, floating point numbers through special convention, or decimal format ranging from 0~99999999. The abbreviation for long word is LW, such as LW0, LW1, etc. A long word occupies two continuous registers in the computer memory. The first register contains the most significant 16 bits (usually referred to as HIGH WORD), the second register contains the least significant 16 bits (usually referred to as LOW WORD). A long word is referenced by the address occupied by the High Word.

Floating Point Representation using a Long Word:

A long word (32 bits) can be used to represent a floating point number. The bit assignment is shown in the following figure:



Formula: $\mathbf{I} = (-1)^{S} \times 2^{(E-64)} \times \mathbf{Fr}$

For example, assuming that the content of register 40130 is C000h and register 40131 is 0042h; then for an operation using floating point referencing register 40130 (40130 and 40131 actually), the value used is:

40130	40131
1100 0000 0000 0000	0000 0000 0100 0010

 $\mathbf{I} = (-1)^{0} \times 2^{(66-64)} \times (2^{-1}+2^{-2}) = 3$

SECTION 2: Constituents of a Function Block

In Embedded PLC series, a function block is composed of four parts: Function Name, Input Control, Operand and Function Output as shown in the following figure:



Function Name:

The function name is an abbreviation or acronym of the operation performed by the function block. Two to four characters are used to represent the function. A complete list of the function block names may be found in the FOREWORD of this manual.

Input Control:

There must be one input control for each function block. This input control (usually referred to as I_1) is used to determine whether to execute this function block or not. For some function blocks, there are two additional input controls (I_2 and I_3). They are used to determine the execution mode of the function block.

Function Output:

There must be a function output control for each function block. This output (usually referred to as O_1) is used to drive a coil or used as an input control for the next function block. For some function blocks, there are two additional output controls (O_2 and O_3), they are also used to represent the results of the execution.

Operands:

Operands, as the name implies, are the objects of operations. An operand whose content is not altered by the operation is called a SOURCE. An operand that is used to store the result of the operation is called a DESTINATION. Operands can be Input contact, Output coil or register in memory. For Embedded PLC, the designations of operands are listed in the following table:

Initial	NAME	DESCRIPTION
0	Output Coil (Discrete output)	Use Output coil as an operand. Since 1 word = 16 bits, thus the number assignment of the operand must be a multiple of 16 plus 1. For example: 00001, 00017, 00033.
1	Input contact (Discrete input)	Use Input contact as an operand. The number assignment of the operand must be a multiple of 16 plus 1. For example: 10001, 10017, 10033.
3	Input register	Use Input register as an operand. For example:30001, 30003.
4	Holding register	Use Holding register as an operand. For example:40001, 40003.
С	Constant	For some function blocks, a constant can be defined as an operand: and during control program execution, the value of the constant is readily available rather then fetching from register memory. For example: #00001, #0020h. The former is a decimal constant, and the latter is a hexadecimal constant.
Р	Pointer	For some function blocks, a pointer can be defined as an operand, and this pointer can be used for indirect addressing pointing to 0-, 1-, 3-, 4-type variable. For example: P0001
L	Label	For paired instructions (such as FOR and NEXT), their operands are label, and the label for each instruction must be the same in order for program to be executed correctly. For example: L0001.

Currently, there are three models of Embedded-PLC controllers. The memory size and the CPU capability are different between models to meet different control requirements. Therefore, the numbers of spaces available for operands are also different. The available ranges for operands for each model are listed in the following table.

Table 1.2: Available operand ranges for different models of the Embedded PLC series controller

OPERAND	Embedded PLC
0	00001~09984
1	10001~12048
3	30001~30512
4	40001~49999
L	L1~L150
С	0~65535
Р	P0~P15

CHAPTER 2: CONTACTS

Contact elements are the most fundamental elements in Ladder Programs. Familiarization with their characteristics and usage is highly recommended.

(1) + - Normally Open Contact:

This type of contact is usually referred to as "A Contact". When a contact is energized, the said "A contact" becomes conductive; and vice versa.



[Meaning]

When input contact 10001 is 'ON', coil 00001 is energized, and "A contact" 00001 becomes conductive, thus, coil 00002 is energized.

(2) - | / | Normally Closed Contact:

This type of contact is usually referred to as "**B** Contact". When a contact is not energized, the said "B contact" becomes conductive; and vice versa.



[Meaning]

When input contact 10001 is 'OFF', coil 00001 is energized, and "B contact" 00001 becomes non-conductive, thus, coil 00002 is not energized.

(3) -()- Output Coil:

This output coil reflects the state of the elements connected to it. If the element is in the 'ON' state, then this coil is said to be energized; and vice versa.



【Timing diagram】



(4) -(S)- Set Coil :

When the element connected to this coil is 'ON', then this set coil is set to 'ON' and remains in that "ON' state until the "RESET coil" with the same reference number is energized.



[Meaning]

When contact 10001 is 'ON', the set coil 00001 is 'ON' and remains 'ON' no matter how contact 10001 is changed.

[Timing diagram]



(5) -(R)- Reset coil:

When the element connected to this coil is 'ON', then this set coil is set to 'OFF' and remains in that "OFF' state until the "SET coil" with the same reference number is energized.



[Meaning]

When input contact 10001 is 'ON', output coil 00001 is set to 'ON' and remains in that state. Until input contact 10001 is 'OFF' and input contact 10002 is 'ON', then output coil 00001 is set to 'OFF' and remains 'OFF'.

[Timing diagram]



(6) -([†])- Positive Transitional Pulse Output Coil:

When the element connected to this output has an 'OFF'□'ON' transition, a pulse('OFF'□'ON') is generated for this output.

(EXAMPLE) (10001 00001

[Meaning]

When input contact 1000 receives a transition 'OFF'□'ON', then a pulse 'OFF'□'ON' is generated for output coil 00001. The width of the pulse is 1 scan time.



(7) $-(\downarrow)$ - Negative Transitional Pulse Output Coil:

When the element connected to this output has an 'ON' OFF' transition, a pulse ('OFF 'ON') is generated for this output.



[Meaning]

When input contact 10001 receives a transition 'ON' OFF', then a pulse 'OFF' ON' is generated for output coil 00001. The width of the pulse is 1 scan time.



(8) -(M)- Holding Coil during power loss:

This output coil reflects the state of the elements connected to it. If the element is in the 'ON' state, then this coil is said to be energized; and vice versa. The last state of the coil is maintained after system power is shut down and turned on again.

(9) -(SM)- Holding Set Coil during power loss:

When the element connected to this coil is 'ON', then this coil is set to 'ON' and remains in that "ON' state until the "RESET coil" with the same reference number is energized. The last state of the coil is maintained after system power is shut down and turned on again.

(10) -(RM)- Holding Reset Coil during power loss:

When the element connected to this coil is 'ON', then this coil is set to 'OFF' and remains in that "OFF' state until the "SET coil" with the same reference number is energized. The last state of the coil is maintained after system power is shut down and turned on again.

CHAPTER 3: FUNCTION BLOCKS



 $\begin{array}{c} O_2: \\ O_3: \end{array} \qquad \qquad \square$

The template for the description of a function block is divided into ten areas ($\square \sim \square$). The meaning for each area is described as follows:

□ NAME:

NAME is an abbreviation or acronym for the operation performed by the function block. Two to four characters are used to represent the function. When displaying the ladder program on screen, the name of the function block is also displayed.

Full Name of Function Block:

The operation of the function block is given briefly in this area.

□ NAME:

This area is provided for easy reference to function blocks.

Trigger mode:

The entry here is used to indicate the trigger mode of the function block. For "Level trigger" mode, when I1 is HIGH, then the function block is executed. For "Edge trigger" mode, when there is an OFF to ON transition, then the function block is executed. For edge-trigger function blocks, a "^" mark is prefixed to the name of the function block in the PP programming environment.

□ Symbol:

The symbol of the function block as used in this manual is displayed in the ladder diagram.

□ Operands

Operands available for the function block have a circle "O " marked in the table.

□ Function block description:

A brief description of the major function of the function block together with its input control, function output and result of the execution is given in this area.

□ Node description:

The usage of each node, whether it is a Source or a Destination, is given in this area.

□ Input Control:

The condition(I_1) required for the function block to be executed is described here. The execution mode (I_2 and/or I_3) is also described here.

Function Output:

The results of the execution (O_1, O_2, O_3) are given in this area.



Description:

Timer increments by one at intervals of one second. When the accumulated time (stored in the BOTTOM node) reaches the timer preset (stored in TOP node), the timer stops. Input control can be used to start, stop and reset the timer. The timer status (whether the elapsed time has reached the preset time) can be detected by examining the function output.

Node description:

TOP: Preset value for timer.

BOTTOM: Accumulated value since timer started.

Input Control:

- I_1 : Execution control. When $I_1 = 1$, timer starts; $I_1 = 0$, timer stops.
- I_2 : Reset control, when $I_2 = 0$, the accumulated value is cleared to zero.

Function Output:

- $O_1 = 1$, if accumulated value = preset value.
- =0, if accumulated value < preset value.
- O₂: Complement of O₁

(EXAMPLE)



[DESCRIPTION]

This example shows a five-second timer. The decomposition of actions are:

- 1. 40012 is 0, then 00040='OFF' and 00041='ON' at the beginning.
- 2. When input control 10012 is 'ON', register 40012 increments by one for every one second.
- 3. When the content of register 40012 = 5 (as defined in the top node), the function output: 00040 = `ON', 00041 = `OFF'.
- 4. Since 00040 = 'ON', I_2 changes to 'OFF', and clears register 40012 to '0'.
- 5. Since 40012 = 0, then 00040 = 'OFF', 00041 = 'ON', register 40012 continues incrementing, and the execution continues from STEP 3.



Timer increments by one at intervals of 0.1 second. When the accumulated time (stored in the BOTTOM node) reaches the timer preset (stored in TOP node), the timer stops. Input control can be used to start, stop and reset the timer. The timer status (whether the elapsed time has reached the preset time) can be detected by examining the function output.

Node description:

TOP: Preset value for timer.

BOTTOM: Accumulated value since timer started.

Input Control:

- I_1 : Execution control. When $I_1 = 1$, timer starts; $I_1 = 0$, timer stops.
- I_2 : Reset control, when $I_2 = 0$, the accumulated value is cleared to zero.

Function Output:

- $O_1 = 1$, if accumulated value = preset value.
- =0, if accumulated value < preset value.

O₂: Complement of O₁

(EXAMPLE)



[DESCRIPTION]

This example shows a five-second timer. The decomposition of actions are:

- 1. 40012 is 0, then 00040='OFF' and 00041='ON' at the beginning.
- 2. When input control 10012 is 'ON', register 40012 increments by one for every one second.
- 3. When the content of register 40012 = 50 (as defined in the top node), the function output: 00040 = `ON', 00041 = `OFF'.
- 4. Since 00040 = 'ON', I₂ changes to 'OFF', and clears register 40012 to '0'.
- 5. Since 40012 = 0, then 00040 = 'OFF', 00041 = 'ON', register 40012 continues incrementing, and the execution continues from STEP 3.



Description:

Timer increments by one at intervals of 0.01 second. When the accumulated time (stored in the BOTTOM node) reaches the timer preset (stored in TOP node), the timer stops. Input control can be used to start, stop and reset the timer. The timer status (whether the elapsed time has reached the preset time) can be detected by examining the function output.

Node description:

TOP: Preset value for timer.

BOTTOM: Accumulated value since timer started.

Input Control:

- I_1 : Execution control. When $I_1 = 1$, timer starts; $I_1 = 0$, timer stops.
- I_2 : Reset control, when $I_2 = 0$, the accumulated value is cleared to zero.

Function Output:

 $O_1 = 1$, if accumulated value = preset value.

=0, if accumulated value < preset value.

 O_2 : Complement of O_1

(EXAMPLE)



[DESCRIPTION]

This example shows a five-second timer. The decomposition of actions are:

- 1. 40012 is 0, then 00040 = 'OFF' and 00041 = 'ON' at the beginning.
- 2. When input control 10012 is 'ON', register 40012 increments by one for every 0.01 second.
- 3. When the content of register 40012 = 500 (as defined in the top node), the function output: 00040 = `ON', 00041 = `OFF'.
- 4. Since 00040 = 'ON', I_2 changes to 'OFF', and clears register 40012 to '0'.
- 5. Since 40012 = 0, then 00040 = 'OFF', register 40012 continues incrementing, and the execution continues from STEP 3.



<u>Node description:</u> TOP: Preset value for counter.

BOTTOM: Accumulated value since counter started.

Input Control:

- I_1 : Counter control. When I_1 receives an 'OFF' \square 'ON' transition, The counter is incremented by 1.
- I_2 : Reset control. When $I_2 = 0$, the accumulated value is cleared to zero.

Function Output:

- $O_1 = 1$, if accumulated value=preset value.
- =0, if accumulated value < preset value.
- O₂: Complement of O₁



[DESCRIPTION]

When contact 10001 receives an OFF to ON transition, the accumulated value of the counter (40001) is incremented by 1. When the accumulated value reaches 100, coil 00001 is energized. When normal-close contact 00001 opens, the counter is reset.



Node description:

TOP: Preset value for counter.

detected by examining the function output.

BOTTOM: Accumulated value since counter started.

Input Control:

- I_1 : Counter control. When I_1 receives an 'OFF' \square 'ON' transition, the counter is decreased by 1.
- I_2 : Reset control. When $I_2 = 0$, the accumulated value is set to preset value.

Function Output:

 $O_1 = 1$, if accumulated value=0.

=0, if accumulated value > 0.

 O_2 : Complement of O_1

(EXAMPLE)



[DESCRIPTION]

When contact 10001 receives an OFF to ON transition, the accumulated value of the counter (40001) is decreased by 1. When the accumulated value reaches 0, coil 00001 is energized. When normal-close contact 000010pens, the counter is reset, and the value in counter (40001) is set to 100.





[DESCRIPTION]

When the contact 10025 has an "OFF \rightarrow ON", the content of register 40021 is added to the content of register 40027 and the sum is stored back to register (40021). Since the sum is larger than 9999, therefore, the second adder is energized.







[DESCRIPTION]

When contact 10025 is energized, the content of register 40021 is added to the content of register 40027 and the sum is stored back to register 40021. Since the sum is larger than 65535, therefore, the second adder is energized.







[DESCRIPTION]

When contact 10025 is "ON', the content of registers 40027/40028 is added to the content of registers 40021/40022. The sum is stored in registers 40030/40031. Since the sum is less then 99999999, thus, O_1 : ON, $O_2 = O_3 = OFF$.







[DESCRIPTION]

When contact 10025 is "ON', the content of registers 40027 & 40028 is added to the content of registers 40021/40022. The sum is stored in the registers 40030 & 40031.






[DESCRIPTION]

When contact 10025 is "ON", the content of registers 40010/40011 is added to the content of registers 40020/40021; the sum is stored in registers 40030/40031; and O_1 : ON, $O_2 = O_3 = OFF$.







[DESCRIPTION]

Assume that register $(40001)=9000_{(10), and}(40002)=500_{(10)}$. when contact 00080 is 'ON', the subtraction: (40003)=(40001) - (40002) is performed. Since the minuend is larger than the subtrahend, thus coil 00011 is 'ON', 00012 is 'OFF' and 00013 is 'OFF'.

40001	09000		9000
40002	00500	_	- 0500
40003	08500	←	8500





[DESCRIPTION]

Assume that register (40001)=9000(10), and (40002)=9000(10). when contact 00080 is 'ON', the subtraction: (40003)=(40001) - (40002) is performed. Since the minuend is equal to the subtrahend, thus coil 00012 is 'ON'.

40001	09000	├	9000
40002	09000		- 9000
40003	00000	◄	0000





[DESCRIPTION]

Assume that long word(40010)=9999(10) and long word(40020)=9999(10.). when contact 00080 is 'ON', the operation: long word(40030)=long word(40010) - long word(40020) is performed. Since the minuend is equal to the subtrahend, thus coil 00012 is 'ON'.

40010	0000
40011	9999
40020	0000
40021	9999
40030	0000
T0030	0000
40031	0000





[DESCRIPTION]

Assume that long word(40010)= $65536_{(10)}$ and long word(40020)= $65536_{(10)}$. when contact 00080 is 'ON', the operation: long word(40030)=long word(40010) - long word(40020) is performed. Since the minuend is equal to the subtrahend, thus coil 00012 is 'ON'.

40010	0001	
40011	0000	
40020	0001	
40020	0001	
40021	0000	
40030	0000	
40031	0000	





[DESCRIPTION]

When contact 10025 is "ON', the content of registers 40020/40021 is subtracted from the content of registers 40010/40011; the difference is stored in registers 40030/40031. Since the minuend is greater than the subtrahend, thus O_1 : ON, $O_2 = O_3 = OFF$.







[DESCRIPTION]

Let register(40005)=2500 and (40006)=1100. When contact 10007 is 'ON', the operation: long word (40036)=(40005)×(40006) is performed.







[DESCRIPTION]

Let register(40005)=2500 and (40006)=1100. When contact 10007 is 'ON', the operation: long word (40036)=(40005)×(40006) is performed.







[DESCRIPTION]

Let register(40005)=25 and (40006)=100. When contact 10007 is 'ON', the operation: (40036)=(40005)×(40006) is performed.







[DESCRIPTION]

Let register(40005)=9999 and (40006)=2. When contact 10007 is 'ON', the operation: (40036)=(40005)×(40006) is performed.







[DESCRIPTION]

Let register(40010/40011)=12345 and (40020/40021)=11. When contact 10007 is 'ON', the operation: long word(40030)=long word(40010)×long word(40020) is performed.

40010	0001
40011	2345
40020 40021	0000 0011
40030	0013
40031	5795

DECIMAL





[DESCRIPTION]

Let register (40010/40011) = 65535 and (40020/40021) = 11. When contact 10007 is 'ON', the operation: long word $(40030) = \log word(40010) \times \log word(40020)$ is performed.

40010 40011	0000 65535	40010 40011	0000 FFFF	
40020 40021	0000 0110	40020 40021	0000 006E	
40030 40031	0109 65426	40030 40031	006D FF92	
	DECIMAL	HE	EXADECIM	<u>1AL</u>





[DESCRIPTION]

Let register (40010/40011)=5000 and (40020/40021)=2. When contact 10025 is 'ON', the operation: long word(40030)=long word $(40010)\times$ long word(40020) is performed. Function Output: $O_1 = ON$, $O_2 = O_3 = OFF$.





The value stored in the top node is divided by the value in the middle node, and the result is stored in the bottom node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Function outputs can be used to determine whether the function block has been executed, divisor is zero and overflow.

Node Description:

TOP: Constant dividend, must be $\leq = 9999$; else the LONG WORD value is used.

MIDDLE: Divisor, must be $\leq = 9999$

BOTTOM: 1.Result of Division. The quotient is stored in the first word. **Depending on the input**

control, the remainder or the first four digits after decimal point of quotient are stored in the second word.

2.If error occurred, the content of the bottom node remains unchanged.

Input Control:

I₁: When \square (\square) is presented, the function block is executed.

 $I_2 = 0$, the second word of the bottom node is used to store the remainder.

= 1, the second word of the bottom node is used to store the first four digits after the decimal point.

 I_3 : error in

Function Output:

 $O_1 = I_1$

 $O_2 = 1$, if overflow, i.e. quotient > 9999

 O_3 (error output)= 1 (1. If the value of either top node or middle node is greater than 9999 or

2.If divisor = 0)



(DESCRIPTION)

Let long word(40090)=9999 and (40130) =10. When contact 10005 is energized, I_1 and I_2 = 'ON'. The quotient (=999) is stored in register 40053. Since I_2 = 'ON', thus the first four digits (=9000) are stored in register 40054.







[DESCRIPTION]

Let **long word**(40090)=65535 and (40130) =12. When contact 10005 is energized, $I_1 = \text{'ON'}$, and the quotient (=5461) is stored in register 40053. Since $I_2 = \text{'OFF'}$, the remainder (=0003) is stored in register 40054.





 O_3 (error output) = 1 (1. If divisor = 0 or

2. If the value of either top node or middle node is greater than 9999.)



[DESCRIPTION]

Let long word(40090)=9999 and (40130) =10. When contact 10005 is energized, I_1 and I_2 = 'ON'. The quotient (=999) is stored in register 40053. Since I_2 = 'ON', thus the first four digits (=9000) are stored in register 40054.



9999÷10 = 999.9000





[DESCRIPTION]

Let long word (40090)=65535 and (40130) =12. When contact 10005 is energized, $I_1 = 'ON'$, and the quotient (=5461) is stored in register 40053. Since $I_2 = 'OFF'$, the remainder (=0003) is stored in register 40054.





Description:

The value stored in the top node is divided by the value in the middle node, and the result is stored in the bottom node. Input control (I_1) is used to determine whether this function block is to be executed or not. Function outputs can be used to determine whether the function block has been executed, divisor is zero and overflow.

Node Description:

TOP: Dividend, must be <= 99999999.

MIDDLE: Divisor, must be <= 99999999.

BOTTOM:1.Result of Division. The quotient is stored in the first and second words. Depending on the input control, the remainder or the first eight digits after decimal point of quotient are stored in the third and fourth words.

2.If error occurred, the content of the bottom node remains unchanged.

Input Control:

I₁: When \Box (\Box) is presented, the function block is executed.

 $I_2 = 0$, the third and the fourth words of the bottom node is used to store the remainder.

=1, the third and the fourth words of the bottom node is used to store the first eight digits after the decimal point.

 I_3 : error in

Function Output:

 $O_1 = I_1$ $O_2 = 0$ O_3 (error output)= 1 (1. If divisor = 0 or

2. If the value of either top node or middle node is greater than 99999999)



[DESCRIPTION]

Let long word(40090)=99999999 and long word(40130) =11. When contact 10005 is energized, $I_1 = 'ON'$, and the quotient (=9090910) is stored in the long word 40053. Since $I_2 = 'OFF'$, the remainder (=0001) is stored in register 40055 and 40056.





 $O_3 = 1$, if divisor = 0.


[DESCRIPTION]

Let long word(40090)=305419896 and long word(40130) =1100. When contact 10005 is energized, $I_1 = 'ON'$, and the quotient (=277654) is stored in long word 40053. Since $I_2 = 'OFF'$, the remainder (=0000) is stored in register 40055 and 40056.







[DESCRIPTION]

Let register(40010/40011)=5000 and (40020/40021)=2. When contact 10025 is 'ON', the operation: long word(40030)=long word(40010)÷long word(40020) is performed. Function Output: $O_1 = ON$, $O_2 = O_3 = OFF$.





Node Description:

TOP: An integer whose square root is desired. BOTTOM: Square root.

Input Control:

I₁: When \Box (\uparrow) is presented, the function block is executed.

Function Output:

 $O_1 = I_1$

 $O_2 = 0$



[DESCRIPTION]

Let (40120)=400. When this rung is scanned, the square root of the values stored in the top node are stored in the bottom node.







[DESCRIPTION]

Let (40120/40121)=2500. When this rung is scanned, the square root of the values stored in the top node are stored in the bottom node (40130/40131).





Description:

The content of the top node is filled onto the table defined in the following address(es) by the middle node. Table length is defined in the bottom node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Input control (I_2) is used to define the action mode of the INDEX.

Input control (I_3) is used to clear the INDEX.

Function outputs can be used to determine whether the function block has been executed and whether the INDEX exceeded the table length.

Node Description:

TOP: Source register.

MIDDLE: Reference register. First word defined as INDEX into the target table. If the value of the INDEX is equal to zero, then the INDEX is pointing to the first entry in the target table. The target table starts with the second word.

BOTTOM: Table Length. If the INDEX value is greater than or equal to this number, table movement is prohibited disregarding the state of I₁.

Input Control:

I₁: When \Box (\Box) is presented, the function block is executed.

I₂: INDEX control.

=0, INDEX is incremented by one after each execution.

=1, INDEX remains unchanged.

I₃: Reset INDEX.

=1, clear INDEX to 0.

Function Output:

 $O_1 = I_1$

O₂: INDEX indicator.

=1, INDEX \geq table length, the INDEX is pointing to an address beyond table limit.



[DESCRIPTION]

When contact 10005 is energized, the content of input register 40120 is copied to table registers (40101~40110), one register per scan. During the action INDEX(40100) increments by one after each scan.

When INDEX (in 40100) reaches preset value of BOTTOM node (#00010), then coil 00001 is energized and the content of register 40100 is cleared. The movement continues until contact 10005 is OFF.





I₂: INDEX control.

=0, INDEX is incremented by one after each execution.

=1, INDEX remains unchanged.

I₃: Reset INDEX.

=1, clear INDEX to 0.

Function Output:

 $O_1 = I_1$

O₂: INDEX indicator.

=1, INDEX \geq table length, the INDEX is pointing to an address beyond table limit.



[DESCRIPTION]

When contact 10054 is energized, source data pointed to by INDEX(40101) is moved to 40201 (the next address defined by the middle node). For every scan of the PLC controller, data movement occurs once until the INDEX reaches the end of table(#00050). Then Coil 00129 is energized and the INDEX is cleared. In this manner, data movement can be repeated. The following is the state after the nth scan since INDEX reset to 0.





Description:

The content of the top node is moved to the following address(es) defined by the middle node. Table length is defined in the bottom node. Input control (I_1) is used to determine whether this function block is to be executed or not. Input control (I_2) is used to define the action mode of the INDEX. Input control (I_3) is used to clear the INDEX. Function outputs can be used to determine whether the function block has been executed and whether the INDEX exceeded the table length.

Node Description:

TOP: Source table.

MIDDLE: INDEX is defined at the first word. If the value of the INDEX is equal to zero, then the INDEX is pointing to the first entry in the target register. The target table starts at the second word. BOTTOM: Table Length. If the INDEX value is greater than or equal to this number, table movement is prohibited disregarding the state of I₁.

Input Control:

I₁: When \square (\square) is presented, the function block is executed.

I₂: INDEX control.

=0, INDEX is incremented by one after each execution.

=1, INDEX remains unchanged.

I₃: Reset INDEX.

=1, clear INDEX to 0.

Function Output:

 $O_1 = I_1$

O₂: INDEX indicator.

=1, INDEX \geq table length, the INDEX is pointing to an address beyond table limit.



[DESCRIPTION]

When contact 10001 is energized and after six scan cycles, data stored in registers $30001 \sim 30006$ are moved to registers $40101 \sim 40106$. When the INDEX in the middle node = 6, the coil 00001 is energized and the INDEX is cleared. Data movement continues until contact 10001 is OFF.



After 6 scans



Description:

This function compares the tables pointed by the top and middle node. If a difference is found between the corresponding table locations, then that the index of that element is stored in the middle node. Table length is in the bottom node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Input control (I_2) is used to define the action mode of the INDEX.

Function outputs can be used to determine whether the function block has been executed and whether those tables are different or not.

Node Description:

TOP: Starting address of the first table.

MIDDLE: INDEX is defined in the first word. The starting address of the second table is defined in the second word. The INDEX has two uses. First, it is used to indicate the index of the difference data after the last comparison. Second, it is used to indicate the next comparison will start below this index. BOTTOM: Table length.

Input Control:

I₁: When (\uparrow) is presented, the function block is executed. Based on the state of I₂, comparison continues until a difference is found or the end of table is reached. When execution terminates, the INDEX points to the address where the difference is found or zero (if no difference).

I₂: Action mode of INDEX.

=0, start comparison from the next address pointed to by the INDEX.

=1, start comparison from the first entry of the table.

Function Output:

 $\mathbf{O}_1 = \mathbf{I}_1$

 $O_2 = 1$, if tables are different.

 O_3 is the complement of $O_{2.}$



[DESCRIPTION]

When contact 10005 is energized, the table starting from 40211 is compared against the table starting from 40451. Since the 6^{th} entries in both tables are different, then register 40450=00006, and coil 00001='ON'.





Description:

Reference value is defined in the second word of the middle node. Search starts from the table defined in the top node. If the same value as the reference is found, then this function stops and the INDEX where the same value was found is stored in the middle node. Table length is defined in the bottom node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Input control (I_2) is used to define the action mode of the INDEX.

Function outputs can be used to determine whether the function block has been executed and whether a same value with the reference has been found.

Node Description:

TOP: Starting address of the table.

MIDDLE: INDEX is defined in the first word. The reference value is defined in the second word of the middle node. The INDEX has two uses. First, it is used to indicate the index of the same data after the last search. Second, it is used to indicate the next search will start below this index.

BOTTOM: Table length.

Input Control:

I₁: When (\uparrow) is presented, the function block is executed. Based on the state of I₂, search continues until a match is found or the end of the table is reached. When execution terminates, the INDEX points to the address where the difference is found.

I₂: Action mode of INDEX.

=0, start search from the next address pointed to by the INDEX.

=1, start search from the first entry of the table.

Function Output:

 $O_1 = I_1$

 $O_2 = 1$, if tables are different.



[DESCRIPTION]

When I1=1, search starts from the table starting from 40211. INDEX(40206) reset to 0 at the beginning. If a table entry is found to be the same as the value of 40207 (next address defined in the middle node), in this example, (40216)=00666=(40207), then 00006 is stored in the middle node, and coil 00010='ON'.

40206	00006	•
40207	00666	•
		INDEX
40211	00111	← 1
40212	00222	← 2
40213	00333	← 3
40214	00000	← 4
40215	00000	← 5
40216	00666	← 6
40217	00000	← 7
40218	00000	← 8
40219	00000	← 9
40220	00999	← 10



(EXAMPLE 1)



[DESCRIPTION]

When contact 10001 receives a transition from 'OFF' to 'ON' and $I_2=I_3=1$, then a right rotate operation is performed.







[DESCRIPTION]

When 10001 receives a transition from 'OFF' to 'ON', then the entries of the two tables as defined in the top and middle nodes are swapped.

		•	
40010	01111	40020	06666
40011	02222	40021	07777
40012	03333	40022	08888
40013	04444	40023	09999
40014	05555	40024	00000
		<u>BEFORE</u>	
10010			01111
40010	06666	<u>BEFORE</u> 40020	01111
40010 40011	06666 07777		01111 02222
		40020	
40011	07777	40020 40021	02222
40011 40012	07777 08888	40020 40021 40022	02222 03333

AFTER

														BL	KM
BLKM	M MEMORY BLOCK MOVE										⊥				
SYMBOL: OPERANDS:															
<u>STRIDOL.</u>				<u>OI ERAIQE.</u>	0	1	3	4	С	P	L				
I ₁ — TOP MIDDI		— O ₁		ТОР	0	0	0	0		0					
I ₂ — BLKN		$- O_2$		MIDDLE	0			0		0					
	DM	[—] O ₃		BOTTOM					1						
				①1~255											
Description:															
Memory contents of	the	table d	efined in	the top node are o	conie	ed to	the	tabl	e de	fined	1 in	the mi	ddle	e no	le in
one scan. Table leng					opie	u to	the	tuoi	e ue	11100	<i>a</i> 111		uui	0 1100	
							•								
	user	u to uc		vhothor this timeti	on h	Incl	10 17	he he	AVA	cute	d or	not			
	ha	wood to		whether this functions whether the function											
Function outputs ca	ı be	used to		ne whether this function											
runction outputs ca	ı be	used to													
runction outputs ca	ı be	used to													
runction outputs ca	ı be	used to													
runction outputs ca	n be	used to													
runction outputs ca	n be	used to													
	ı be	used to													
Node Description:			o determi	ne whether the fu											
<u>Node Description:</u> TOP: Starting addre	ss of	f the so	o determi	ne whether the fur											
<u>Node Description:</u> TOP: Starting addre MIDDLE: Starting a	ss of	f the so ess of t	o determi	ne whether the fur											
<u>Node Description:</u> TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le	ss of	f the so ess of t	o determi	ne whether the fur											
Node Description: TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le Input Control:	ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								
<u>Node Description:</u> TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le	ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								
Node Description: TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le Input Control:	ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								
Node Description: TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le Input Control:	ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								
Node Description: TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le Input Control: I ₁ : When (ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								
Node Description: TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le Input Control: I ₁ : When (Function Output:	ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								
Node Description: TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le Input Control: I1: When \Box () Function Output: $O_1 = I_1$	ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								
Node Description: TOP: Starting addre MIDDLE: Starting a BOTTOM: Block le Input Control: I ₁ : When (Function Output:	ss of addre	f the so ess of t	o determi urce mer he target	ne whether the fur nory block. memory block.	nctio	n blo	ock								



[DESCRIPTION]

When 10001 receives a transition from 'OFF' to 'ON', then the entries of the first tables as defined in the top node(40211) are moved to the second table defined in the middle node(40451).

40211	01111
40212	02222
40213	03333
40214	04444
40215	05555
40216	06666
40217	07777
40218	08888
40219	09999
40220	00000
	BEFORE
	LI
40451	00000
40452	00000
40452 40453	00000 00000
40453	00000
40453 40454	00000
40453 40454 40455	00000 00000 00000
40453 40454 40455 40456	00000 00000 00000 00000
40453 40454 40455 40456 40457	00000 00000 00000 00000 00000
40453 40454 40455 40456 40457 40458	00000 00000 00000 00000 00000
40453 40454 40455 40456 40457 40458 40459	00000 00000 00000 00000 00000 00000
40453 40454 40455 40456 40457 40458 40459 40460	00000 00000 00000 00000 00000 00000 0000

40451	01111
40452	02222
40453	03333
40454	04444
40455	05555
40456	06666
40457	07777
40458	08888
40459	09999
40460	00000

⇔

AFTER



Stack size is defined in the BOTTOM NODE.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Input control (I_2) is used to define the stack mode.

Function outputs can be used to determine whether the function block has been executed, and whether the stack is full.

Node Description:

TOP: Data to be pushed into stack.

MIDDLE: INDEX is defined in the first word. The starting of the stack is defined in the second word of the middle node. If the INDEX is equal to zero, then the INDEX is pointing to the top of the stack. BOTTOM: Length of stack.

Input Control:

I₁: When (\uparrow) is presented, the function block is executed. INDEX is incremented by 1.

 $I_2 = 0$, data is pushed into the stack at designated address. (LIFO). Used as STACK.

=1, data is put into the bottom of the stack , the original content at the top of the stack is moved to the next word. (FIFO). Used as QUEUE.

Function Output:

 $O_1 = I_1$ O_2 : =1, if stack is full, i.e. INDEX= stack length. $O_3=0$

[EXAMPLE 1]



(DESCRIPTION)

Since $I_2 = 'OFF'$, thus the operation mode is LIFO (Last In First Out). When I_1 receives an "OFF \rightarrow ON" transition, the PUSH function is executed as follows:



[EXAMPLE 2]



(DESCRIPTION) Since $I_2 = 'ON'$, thus the operation mode is FIFO (First in first out). When I_1 receives an "OFF \rightarrow ON" transition, the PUSH function is executed as follows:



For the next "OFF \rightarrow ON" transition on I₁:





Input control (I_1) is used to determine whether this function block is to be executed or not.

Function outputs can be used to determine whether the function block has been executed, and whether the stack is empty.

Node Description:

TOP: INDEX is defined in the first word. The starting of the stack is defined in the second word of the middle node. If the INDEX is equal to zero, then the INDEX is pointing to the top of the stack.

MIDDLE: Data retrieved from stack.

BOTTOM: stack length.

Input Control:

I₁: When \Box (\uparrow) is presented, the function block is executed. INDEX is decremented by 1 first, then the data is retrieved according to the INDEX.

Function Output:

 $O_1 = I_1$ $O_2 = 0$ $O_3 =$ Stack status. = 1, if stack is empty, i.e. INDEX = 0.



[DESCRIPTION]

When contact 10020='ON', the INDEX (40340) is decremented by 1; then the value pointed by the INDEX is retrieved and stored in the location pointed by the middle node. Through repeated conducting of contact 10020, the values in the stack are POPed successively.





The contents of top and middle nodes are ANDed, and the result is stored in the middle node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Function outputs can be used to determine whether the function block has been executed.

Remark: When the content of the top node is a constant, the constant and the contents of middle node are ANDed and the result is stored in the middle node.

Node Description:

TOP: Source Array 1, or constant.

MIDDLE: Source Array 2, Resultant Array (after processing).

BOTTOM: Length of Array.

Input Control:

I₁: When \Box (\Box) is presented, the function block is executed.

Function Output:

 $O_1 = I_1$ $O_2 = 0$

(EXAMPLE 1)



[DESCRIPTION]

When contact 10025 is energized, the contents of registers 40090 and 40095 are ANDed, and the result is returned to register 40095.





Remark: When the content of the top node is a constant, the constant and the contents of middle node are ORed and the result is stored in the middle node.

Node Description:

TOP: Source Array 1, or constant.

MIDDLE: Source Array 2, Resultant Array (after processing).

BOTTOM: Length of Array.

Input Control:

I₁: When \Box (\Box) is presented, the function block is executed.

Function Output:

 $O_1 = I_1$

 $O_2 = 0$

(EXAMPLE 1)



[DESCRIPTION]

When contact 10025 is energized, the contents of registers 40090 and 40095 are ORed, and the result is returned to register 40095.





Function Output:

 $O_1 = I_1$

 $O_2 = 0$

(EXAMPLE 1)



[DESCRIPTION]

When contact 10025 is energized, the contents of registers 40090 and 40095 are XORed, and the result is returned to register 40095.







[DESCRIPTION]

When contact 10025 is energized, 1's complement is obtained for the content of register 40090, and the result is returned to register 40095.




 $O_1 = I_1$ $O_2=1$, if a difference is found. $O_3=0$



[DESCRIPTION]

When contact 10001 is energized, the matrix starting from 40010 is compared against the matrix starting from 40100. Since the fourth bit is different, then the index of that location is stored in the middle node and coil 00002 is energized.



													BR	OT
BRO	TC	BIT ROT.	ATE/SH	IFT F	OR	M	AT	RIŽ	K				ſ	
SYMBOL:	<u>:</u>		<u>OPER</u> A	ANDS:		1					1	1		
]			0	1	3	4	С	Р	L	-		
I_1 —	TOP	$- O_1$	TC	OP	0	0	0	0		0				
	MIDDLE											-		
I ₂ —	BROT	$- O_2$	MID	DLE	0			0		0				
I ₃ —	BOTTOM	— O ₃	BOT	ТОМ					0					
		1	①1~25	5]		
Description	n:													
		his function perfo	orms array	rotate of	or sh	ift. 7	The	resu	lt is	stor	ed i	n the mic	ldle n	ode.
-		the top node.												
		n the bottom nod	e.											
-		ed to determine w		s function	on bi	lock	is to	b be	exe	cute	d or	not.		
-		ed to define the d												
-		d to define the m												
Function or	utputs can be	used to determin	ne whether	r the fun	ctio	n blo	ock l	has l	seen	exe	cute	ed.		
Node Desc	ription:													
TOP: Source	ce matrix.						SH	IF.	ΓМ	ΙΟΙ	DE			
MIDDLE.	Target matri	x	LEFT	O_2	15	Wn		b 0			b15	\mathbf{W}_1	b0	
	•	natrix (word).				,,,,,,		Ū́+		[0
Input Con		utik (word).								-				
I_1 : Executio			<u>RIGHT</u>		b15	Wı	า	b0			b15	\mathbf{W}_1	b0	0,
		s presented,	Mon						_					Ĭ
rotate/shift	operation is	performed one bi	it per scan											
I ₂ : Direction	n													
=0, Left.														
=1, Righ	t.					F	RO]	[A]	ΓE I	MC	DDE	Ŧ		
I ₃ : Mode.			<u>LEFT</u>	b	15	Wn		<u>b0</u>		b <u>15</u>	;	<u>W1</u>	ьо Т	
=0, Shift				_ f−L				+					┣┻	
=1, Rota	te.													
Function (<u>)utput:</u>		<u>RIGHT</u>	b15	<u>, '</u>	Wn		0		b15	<u> </u>	W ₁ b	D 1.	
$O_1 = I_1$				_ ⊢ L						→∟				
O ₂ : Bit shif	ted into this	position.												
O ₃ =0														



[DESCRIPTION]

When contact 10001 receives a transition from 'OFF' to 'ON' and $I_2=I_3=1$, then a right rotate operation is performed.



													ODSR
ODSR		NIBBLE	ROTATI	E/SHIFT	' F(DR	MA	TF	RIX				
SVMDOL .			ODE	DANDC.									
<u>SYMBOL:</u>			<u>OPE</u>	RANDS:	0	1	3	4	С	Р	L		
	OP	-0		ТОР	0	1	3 0	4	C	r	L		
	DDLE	$-O_1$		IOP									
$I_2 - OI$	DSR	$-O_2$	М	IDDLE	0			0		0			
	TTOM	— O ₃	BC	OTTOM					1				
			01~ 2	255									
Description:													
Using the nibble	as a uni	t, this functio	on performs	array rota	te o	r shi	ft. T	he r	esul	t is s	store	ed in the	middle
node. The matrix	k is defir	ned in the top	node. Tab	le length is	def	ined	in t	he b	otto	m n	ode.		
Input control (I ₁) is use	d to determir	ne whether	this function	on bl	lock	is to	b be	exe	cute	d or	not.	
Input control (I ₂	(2) is use	d to define th	ne direction	•									
Input control (I ₃	3) is used	d to define th	e mode.										
Function outputs	s can be	used to deter	rmine whet	her the fun	ctio	n blo	ock l	has l	been	exe	cute	ed.	
Node Description	<u>)n:</u>												
TOP: Source ma	trix						SH	IFT	ſ M	OD	ЭE		
MIDDLE: Targe	et matrix	Σ.		W	n		V	Vn-1			V	W_1	
BOTTOM: Leng	gth of m	atrix (word)	LEF	<u>r</u>		←			}—	-		∭←	- 0000
Input Control:							1	Vn	-		W	n- 1	W 1
I ₁ : Execution con	ntrol.		<u>RIG</u>	HT 00	00	→	Π	П			∙∏	\square	
When [†) is	s presented,											
rotate/shift opera			e nibble pe	r scan.									
12: Direction	-	-	-		F	RO T	ΓA]	ΓE I	MC)DI	E		
=0, Left				Wn		v	Vn-1				W_2		\mathbf{W}_1
=1, Right			<u>LEFT</u>	┍━[]]]]-	- [□⊷]⊷[
I ₃ : Mode.					-								
=0, Shift.				Wn		v	Vn-1				W_2		\mathbf{W}_1
=1, Rotate.			<u>RIGHT</u>	⊢]—	• [<u> </u>	→]→[
Function Outpu	<u>ıt:</u>				-								
$O_1 = I_1$	_												
$O_2 = 0$													
O ₃ =0													



[DESCRIPTION]

When contact 10001 receives a transition from 'OFF' to 'ON' and $I_2=I_3=1$, then a right rotate operation is performed.







[DESCRIPTION]

00005 is stored in the top node (40093). When contact 10001 is energized, and $I_2=1$, then the 5th bit of the matrix starting from 40733 to 40735 is set to 1.



BEFORE

AFTER



This function is used to sense a certain bit in a matrix. Bit location is defined in the top node. Matrix to be modified is defined in the middle node. Array length is defined in the bottom node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Input control (I_2) is used to define the behavior of the INDEX.

Input control (I_3) is used to reset the INDEX.

Function outputs can be used to determine whether the function block has been executed, and the status of the INDEX.

Node Description:

TOP: INDEX (pointing to the bit to be checked). INDEX=1 \rightarrow The first bit.

MIDDLE: Source matrix.

BOTTOM: Matrix length (word).

Input Control:

I₁: When \Box (\Box) is presented, the function block is executed.

- I₂ : INDEX control. If I₃=1 and the top node is 4XXXX, then the INDEX is incremented by 1 after execution.
- I₃: INDEX control.

=1, Reset INDEX.

Function Output:

 $O_1 = I_1$

 $O_2 =$ The state of the bit sensed.

O₃: Status of the INDEX.

=1, if INDEX is equal to zero or larger than the value of the BOTTOM node times 16.



[DESCRIPTION]

When contact 10001 is energized and $I_2=1$, the state of coil 00095 is set to that of the bit checked. Since (40421)=0001, The 1st bit is checked. And since the bottom node is #00002, thus the registers 40151~40152 are checked.





TRUTH TABLE

Ι	NP	UTS	S						0	UTI	PUI	г₩	/OF	RD					
3	2	1	0	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
0	1	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
0	1	1	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
1	0	1	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

[EXAMPLE]



[DESCRIPTION]

Let register 40019 = 2CF9h = 0010 1100 1111 1001B, and #00000 is defined in the bottom node. #00000 indicates that the first set of 4-bit data is to be used as the decoder function input. The first 4-bit set in this example is 1001B, which is equal to 9. Therefore, the 10th bit (0 means the 1st bit and 15 means the 16th bit) in the middle node (40009) will be set after contact 10001 is energized.





This function is a 16-bit to 4-bit encoder. The top node contains the data to be encoded. The bottom node indicates the 4-bit set to be used to store the encoded result, and the encoded data is stored in the middle node.

NOTE: If more than one bit is set in the top node, then the bit which is closer to the most significant bit will be used for encoding.

Input control (I_1) is used to determine whether this function block is to be executed or not. Function outputs can be used to determine whether the function block has been executed.

Node Description:

TOP: Input to encoder.

MIDDLE: Encoder result.

BOTTOM: Nibble (0~3) where the encoder result is stored.

Input Control:

I₁: When \Box (\Box) is presented, the function block is executed.

Function Output:

 $O_1 = I_1$ $O_2 = indicator$ =1, if the valued stored in the TOP node is zero. $O_3=0$

TRUTH TABLE

					Ι	NP	UT	W	OR	D						OUTPUTS				
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	3	2	1	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	
0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	1	
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	1	0	
0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	
0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	1	
0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	1	0	
0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	
0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	

(EXAMPLE)



[DESCRIPTION]

Let $40009 = 0040h = 0000\ 0000\ 0100\ 0000h$, and #00000 is given in the bottom node. Since MSB is the 16th bit and LSB is the first bit in a 16-bit register, thus, the 7th bit is encoded to 6; and 6 is equal to 0110B. This 0110B 4-bit set is moved to the 1st 4-bit set of register 40019 as defined in the bottom node(#00000)





= 1, if the valued stored in the TOP node is	>99999(decimal) when the value of bottom node is 1.
= 1, if the valued stored in the TOP node is	> 999999999(decimal) when the value of bottom node is '2'.
$O_3 = 0$	



[DESCRIPTION]

Let register (40100) = 0001h, and register (40101) = 0002h. When contact 10001 is energized, since #00002 is defined as a long word conversion, then the top node long word (40100) 10010h=65538d are converted and stored in registers 40009 and 40010.

					J.	
40100	0000	0000	0000	0001		
40101	0000	0000	0000	0010] /	<u>10010h</u>
40009	0000	0000	0000	0110		
40010	0101	0101	0011	1000] ʃ	<u>65538d</u>
					1	



This function performs binary-coded-decimal to binary conversion. The data to be converted is defined in the top node, and the converted data is stored in the middle node. The bottom node defines the conversion type (word or long word).

Input control (I_1) is used to determine whether this function block is to be executed or not.

Function outputs can be used to determine whether the function block has been executed and whether the result is correct or not.

Node Description:

TOP: data set (BCD) to be converted.

MIDDLE: Conversion result.

BOTTOM:1. Word

2. Long word

Input Control:

I₁: When \Box (\Box) is presented, the function block is executed.

Function Output:

 $O_1 = I_1$ $O_2 = indicator$

= 1, if the valued stored in the TOP node is not in BCD format.

 $O_3 = 0$



[DESCRIPTION]

Science this is a long word conversion (bottom node is #00002). When contact 100d is energized, the top node long word (30009) = 65538d is converted to 10010h and stored in middle node (40019), (40020).

Let register (30009) = 8888d = 22B8h, and register (30010) = 7777d = 1E61h.

When contact 10001 is energized, since #00002 is defined to the bottom node, then the converted BCD codes are stored in registers 40019 and 40020.

					1.	
30009	0000	0000	0000	0110		
30010	0101	0101	0011	1000		<u>65538d</u>
40019	0000	0000	0000	0001		
40020	0000	0000	0000	0010		<u>10010h</u>
					1	

											SSEG
SSEG	SEV	EN-SEGMENT	DE	C O]	DE	R			_		
SYMBOL:		OPERANDS:									
<u>STMDOL.</u>		OI ERAILDS.	0	1	3	4	С	Р	L		
I ₁ — TOP MIDDLI	$-O_1$	ТОР	0	0	0	0		0	L		
$I_2 - SSEG$		MIDDLE	0			0		0			
	$M = O_3$	BOTTOM					1				
		①1~4									
The register for the to display format. The be node. Input control (I ₁) is to Function outputs can	ottom node def used to determi	ines the size (1~4) to ne whether this functi	be co on b	onve lock	erted is to	l. Th	exe	sult	is st d or	ored in t not.	
Node Description:											
TOP: data to be conv	erted.										
MIDDLE: conversior	n result.										
BOTTOM: number o	f digits to be co	p_{vartad} (1 digit -4 bi	ts).								
		mvertea (Taigit – 4 bi									
$\frac{\text{Input Control:}}{I_1: \text{ When } \Box (\)}$				cute	d.						
Input Control: I₁: When 」 _ (_ I₂ : Leading zero disp	lay control			cute	d.						
Input Control: I₁: When ∫ (∫ I₂ : Leading zero disp =0, normal display.	lay control			cute	d.						
Input Control: I₁: When ∫ (∫ I₂ : Leading zero disp =0, normal display. =1, leading zero su	lay control			cute	d.						
Input Control: I₁: When ∫ (↑ I₂: Leading zero disp =0, normal display. =1, leading zero su Function Output:	lay control			cute	d.						
Input Control: I_1 : When I_2 : Leading zero disp $=0$, normal display. $=1$, leading zero suFunction Output: $O_1 = I_1$	lay control			cute	d.						
Input Control: I₁: When ∫ (∫ I₂ : Leading zero disp =0, normal display. =1, leading zero su Function Output:	lay control			cute	d.						



[DESCRIPTION]

When $I_1=1$, the data contained in register 40095 is divided into four 4-bit sets and converted to 7-segment display format. Since the bottom node is given #00004, thus, all four 4-bit sets are converted and stored in the middle node registers 40095 and 40096.



Display	number	а	b	с	d	e	f	g
0	0	1	1	1	1	1	1	0
1	1	0	1	1	0	0	0	0
2	2	1	1	0	1	1	0	1
3	3	1	1	1	1	0	0	1
4	4	0	1	1	0	0	1	1
5	5	1	0	1	1	0	1	1
6	6	1	0	1	1	1	1	1
7	7	1	1	1	0	0	0	0
8	8	1	1	1	1	1	1	1
9	9	1	1	1	1	0	1	1
Α	А	1	1	1	0	1	1	1
b	В	0	0	1	1	1	1	1
С	С	1	0	0	1	1	1	0
d	D	0	1	1	1	1	0	1
Ε	Е	1	0	0	1	1	1	1
\mathbf{F}	F	1	0	0	0	1	1	1

TRUTH TABLE

* Leading zeroes are converted to 0's.



(EXAMPLE 1)



[DESCRIPTION]

When contact 10001 is energized, the content of the top node(40090) is split into two bytes which are stored in the middle node (40095 and 40096)



[EXAMPLE 2]

PACK



[DESCRIPTION]

When contact 10001 is energized, two LOW BYTEs taken from the top node(40090 and 40091), are concatenated to form a new 16-bit word which is stored in the middle node(40095).







[DESCRIPTION]

Converts the integer stored in the top node (40120) to a floating number and stores in the middle node(40130 & 40131). A floating point number is represented by two words: bit0~bit6 represent the exponent, bit7is the sign bit(0: positive, 1:negative), and bit8~bit31 represent the fraction.





	40130
_	F->I
-	40150

[DESCRIPTION]

For a floating point number C000, 0042 stored in registers 40130 and 40131 respectively, the conversion returns 0003 stored in 40150.







[DESCRIPTION]

When contact 10001 is energized, the program between JMP L00001 and EOJ L00001 is skipped. The execution continues from PAGE N. If contact 10001 is not energized, then no JUMP action is performed.

If contact 10001 is not energized, but contact 10002 is energized, then the program between JMP L00002 and EOJ L00002 is skipped.

The execution continues from PAGE M.



											JSR
JSR		JUMP to SUBR(DUT	INI	£						
SYMBOL:		OPERANDS	<u> </u>								
			0	1	3	4	С	P	L		
I ₁ — TOP	$-O_1$	ТОР							1		
JSR											
I ₂ —	— O ₂										
		①L1~L32									
Descriptions											
Description: This instruction is use	ed to call the sub	routine whose label	is the	. con		a the	one	a dat	fined	in the T	on node
Subroutine calls may				/ 5411	iic a	s the		uci	incu		op noue.
Programming require		•		SBI	R ins	struc	tion	is h	ehin	d the IS	R
instruction, and ③RE		_			· ·	, ac		15 0		a the 55	
Input control (I_1) is t					o be	exe	cute	ed or	r not	_	
Function outputs can										•	
									• • • •		
Node Description:											
TOP: Label of the sul	broutine to be c	alled.									
Input Control:											
I ₁ : When $ ($	_) is presented	, the instruction is ex	ecute	ed.							
Function Output:											
$ \begin{array}{l} \mathbf{O}_1 = \mathbf{I}_1 \\ \mathbf{O}_2 = 0 \end{array} $											
02=0											



[DESCRIPTION]

The main program area and the subroutine area are separated by the EOP instruction. If the EOP instruction does not exist, then the first SBR instruction is used as a program delimiter.



[DESCRIPTION]

When contact 10001 is energized, subroutine L00001 is executed. The program control is returned to the main program when RET L00001 is encountered. The execution of the main program is terminated when the EOP instruction is encountered.

During the execution of subroutine L00001, if contact 10002 is energized, then subroutine L00010 is executed until RET L00010 is encountered. When RET L00001 is encountered, the program control returns to the main program.

If contact 10001 is not energized, then neither subroutine L00001 nor L00010 is executed.








[DESCRIPTION]

When contact 10001 is energized, the loop (L00001) is executed 10 times, then the program resumes from page n. If contact 10001 is not energized, the loop (L00001) is skipped. If both contacts 10001 and 10002 are energized, then loop L0011 is executed N times (as defined in the bottom node 40001), and loop L00001 is executed 10 times. If contact 10001 is energized while contact 10002 is not, then the loop (L00001) is executed for 10 times, while loop L0011 is skipped.



CHAPTER 4: FLOW CONTROL INSTRUCTIONS

															EO	P
EC	OP		END o	f MAI	N PRO	GI	RA	Μ								
<u>SYMBOI</u>	<u></u>		-	<u>OPERA</u>		<u> </u>		1					1			
			Г			0	1	3	4	С	Р	L	-			
I_1 —	-	$-O_1$														
	EOD		-													
	EOP															
т.—	-	$-O_2$	-													
I ₂ —																
			L													
Descriptio	on:															
		d to define th	e end of	a progra	am. All t	he p	prog	gran	nmin	ig be	ehino	l thi	s instr	uctio	on is	
		scan termina														
0	1 0															
Node Des	cription:															
11040 200																
Input Cor	ntrol:															
I_1 : Don't c																
Function	Output:															
$O_1 = 0$																
$O_2 = 0$																



(EXAMPLE)



[DESCRIPTION]

When contact 10001 is energized, then the skip instruction is executed.

- 1. If the content of register 40001is #00002, then the next two pages are skipped.
- 2. If the content of register 40001 is 0, then the program execution for this scan is terminated.

Let register (40001)=00002:



The **<u>SKIP</u>** instruction is at the bottom of Page 2.





[DESCRIPTION]

When contact 10001 is energized, the power rail input of ladder program segment between the MCS-MSE function blocks is OFF. If the contact 10001 is not energized, then the ladder program segment is executed as usual.

MSE	MAST	TER CONT	ROJ	LE	ENI	D							
SYMBOL:		OPERANDS :						1					
			0	1	3	4	С	P	L				
I_1 —	$-O_1$												
MSE													
				1				L					
Description:													
This function block is t	the matched ending	g instruction for 1	MCS	func	ctior	n blo	ock.						
Node Description:													
Input Control:													
Function Output:													
O ₁ =0 O ₂ =0													





[DESCRIPTION]

When contact 10001 is energized, the relationship: (P0002)=40001 is defined. This means the (P0002) pointer points to this 40001 register.

												INCP
INC	CP	IN	CREMENT OF	POI	NT	ER				_		
				1								
SYMBOL:	<u>.</u>		OPERANDS		1	2	1	C	р	т		
т	тор		TOD	0	1	3	4	C	P	L		
$I_1 -$	TOP	$-O_1$	TOP						0			
	INCP											
	inter											
I ₂ —		$-O_2$										
-2												
			①P0~P15				1					
Description	<u>n:</u>											
This function	on is used to	increment th	e pointer by one. Th	e cons	stant	in t	he to	op n	ode	defi	nes whic	h pointer is
to be increr	mented.											
Input contr	ol (I_1) is us	ed to determ	ine whether this func	tion b	lock	is to	b be	exe	cute	d or	not.	
Nada Dara												
Node Desc	er to be inci	amontod										
TOP. Folli		ementeu.										
Input Con	trol·											
_) is presented	l, the instruction is e	xecute	ьd							
) is presented	, the list detion is e	Accute	<i>.</i>							
Function (Output:											
$O_1 = I_1$												
O ₂ =Error												
	n this functi	on is called, t	he reference number	pointe	ed by	y the	e po	inter	is a	lrea	dy point	ed to the
		ber of that refe		-	•		-					
			* *									



[DESCRIPTION]

Assume that pointer 2 contains 40001, when 10001=1. Then, pointer 2=40002, i.e. P0002=40002 after execution.





[DESCRIPTION]

When the contact 10001 is energized, the content of pointer P0002 is added 4 and the sum is stored to the content of pointer P0003.





[DESCRIPTION]

Assume that pointer 2 contains 40011, when 10001=1. Then, pointer 2=40010, i.e. P0002=40010 after execution.





[DESCRIPTION]

When the contact of 10001 is energized, the content of pointer P0002 is substrated 4 and the sum is stored to the content of pointer P0003.

											MOVE
MO	VE		DATA MOV	/E							
SYMBOL	•		OPERANDS:								
DINDUL	<u>-</u>			0 1 3 4 C						L	
I_1 —	ТОР	$-O_1$	TOP	0	0	0	0	0	0		
	MOVE										
I ₂ —	воттом	— O ₂	BOTTOM	0			0		0		
		I	00~65535								
Input contr	on is used to ol (I ₁) is use		ntent of a register (4x ne whether this functi								
	renced (or so		or a constant. be initialized (target)								
Input Con) is presented	, the instruction is exe	ecute	ed.						
Function (<u>Dutput:</u>										
$O_1 = I_1$											
O ₂ =0											



[DESCRIPTION]

When contact 10001 is energized, the constant #00100 is stored in register 40001, i.e. (40001)=100.



This function is used to compare the data in the top node and the middle node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Outputs (O_1, O_2, O_3) are represented the comparing result (>, =, <) of top node and the middle node when this function block is executed.

Node Description:

TOP: Top node data.

MIDDLE: Middle node data.

BOTTOM: Length to be compared (1: Word, 2: Long word)

Input Control:

I₁: When \Box (\Box) is presented, the instruction is executed.

Function Output:

 O_1 : comparing result (data of top node > middle node)

 O_2 : comparing result (data of top node = middle node)

O₃ : comparing result (data of top node < middle node)



[DESCRIPTION]

Assumed that the content of register (40001) = 9000(10) and the content of register (40002) = 500(10): When the contact of 00080 is energized, the coil of 00011 will be 'ON' because the content of (40001) > (40002).

CHAPTER 5: SYSTEM RELATED INSTRUCTIONS

												DGET	
DG	ET	GE	T CALEN	CALENDAR DATE									
SYMBOL	<u>:</u>		OPERA										
				0	1	3	4	C	Р	L			
I ₁ —	ТОР	- O1	TO	>			0		0				
	DGET												
$I_2 -$	-	O2											
						<u> </u>							
Description	n:												
		to obtain the systematic	em date. The	esult is sto	ored	in th	ne to	op n	ode	(two	o words).	
		irst word represe						-					
		ord represents dat	-	-					-			-	
-		are in BCD forma			·						-	-	
Input contr	ol (I_1) is t	used to determine	whether this	function b	lock	is to	be	exe	cute	d or	not.		
Node Desc	rintion												
		e the calendar da	te										
101. Roga		e the calendar da											
Input Con	trol:												
) is presented,											
	ction is exe	-		5 14 13	12	11	10	9	8 7	6	5 4 3		
			\mathbf{W}_1	(19	or	20) _{bc}	CD CO	DE			Yea	:s	
			W_2	Month(01~1	12) _{BCI}	D COE	DE		Dat	e(01~3	1) _{BCD CODE}	
Function (<u>Dutput:</u>												
$O_1 = I_1$													
$O_2 = 0$													



[DESCRIPTION]

When contact 10001 is energized, the system date is copied to registers 40001 and 40002 in BCD format. Assume that the date reads:

 $(40001) = 1996_{\rm H}$ $(40002) = 0918_{\rm H}$ Then the date is Sept. 18th, 1996.



Description:

This function is used to set the system date. The date is stored in the top node (two words).

The high byte of the first word represents year; the low byte of the first word represents month, the high byte of the second word represents date in a month; and the low byte of the second word represents day in a week. All numbers are in BCD format.

Input control (I_1) is used to determine whether this function block is to be executed or not. Function outputs can be used to determine whether the function block has been executed.

Node Description:

TOP: Date, two words.

Input Control:

I₁: When \int is presented, the instruction is executed.

Function Output:

 $O_1 = I_1$ $O_2 = 0$

(EXAMPLE)



[DESCRIPTION]

When contact 10001 is energized, the data stored in registers 40001 and 40002 are used to set the system date.

If $(40001) = 1996_{\rm H}$ (40002) = 0918_H the setting date is Sept 18th, 1996.





[DESCRIPTION]

When contact 10001 is energized, the reference date stored registers 40001, and a constant 0111h is compared against the system date for any difference.

If the bottom node is represented by X_1 , X_2 , X_3 , X_4 (hex format) then

 X_0 : reserve

X₁: nibble mask for the Year

X₂: nibble mask for the Month

X₃: Date

```
Assume the system date is Sept. 18th, 1996, and

40001=1996_H

40002=0917_H

Bottom mode =0111<sub>H</sub> - (Day comparison is suppressed.)
```

Since the reference date (17) is smaller than the system date (18), thus, coil 00003 is turned 'ON'.

Note: The date comparison is made according to the following order:

Year->Month->Date

If any difference is found while comparing the higher ranked unit, the function output is set based on the comparison result, and the rest of the data is ignored.

														IU.	EΤ
TGI	T		GET S	YSTEN	A TIN	IE									
SYMBOL:			<u>OP</u>	OPERANDS:											
_ []			0	1	3	4	С	P	L				
I_1 —	TOP	$-O_1$		TOP				0		0					
	TGET														
	1021														
I ₂ —		$-O_2$													
l															
<u>Description</u>		1. 1. 1		T	1. • .		• •			1	<i>.</i>				
This function	on is used to	o obtain the system	stom time		111 10 017	ored	in ti	ne to	op ne	ode	(two) word	S).		
	C (1 C	•							-					1	.1
		st word repres	sents day i	n a week	; the lo	w by	yte c	of th	e fir	st w	ord	represe	nts		
high byte of	the second	st word repres	sents day i	n a week	; the lo	w by	yte c	of th	e fir	st w	ord	represe	nts		
high byte of numbers are	the second in BCD fo	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are	the second in BCD fo	st word repres	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are	the second in BCD fo	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are	the second in BCD fo	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are	the second in BCD fo	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are	the second in BCD fo	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are	the second in BCD fo	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are	the second in BCD fo	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are Input contro	the second in BCD for ol (I_1) is us	st word represe word represe	sents day i nts minute	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are Input contro <u>Node Desc</u>	the second in BCD for ol (I_1) is us	st word represe word represe ormat. sed to determin	ents day i nts minute ne whethe	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are Input contro <u>Node Desc</u>	the second in BCD for ol (I_1) is us	st word represe word represe	ents day i nts minute ne whethe	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are Input contro <u>Node Desc</u> TOP: Regis	the second in BCD fo ol (I ₁) is us <u>ription:</u> ter to store	st word represe word represe ormat. sed to determin	ents day i nts minute ne whethe	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are Input contro <u>Node Desc</u> TOP: Regis <u>Input Cont</u>	the second in BCD fo of (I ₁) is us <u>ription:</u> ter to store	st word represe word represe ormat. sed to determine the system tim	ents day i nts minute ne whethe	n a week e; and the	; the lo low by	w by yte o	yte c of the	of the sec	e firs	st w l wo	ord r rd re	represe epresen	nts		
high byte of numbers are Input contro Mode Descr TOP: Regis Input Cont I ₁ : When _	The second in BCD for of (I_1) is us ription: ter to store	st word represe l word represe ormat. sed to determin the system tim) is presented	ents day i nts minute ne whethe	n a week e; and the r this fund	; the lo	w b <u>y</u> yte o lock	yte c f the is to	f the sec	e firs	st w l wo cute	d or	not.	nts t se		
high byte of numbers are Input contro Mode Descr TOP: Regis Input Cont I ₁ : When _	the second in BCD for of (I ₁) is us <u>ription:</u> ter to store	st word represe l word represe ormat. sed to determin the system tim) is presented	ents day i nts minute ne whethe ne.	n a week e; and the r this fund 15 1	; the lo low by ction b	w by yte o lock	yte c f the is to		e first cond exec	st w wo cute	ord n rd re d or	not.	nts t se		
high byte of numbers are Input contro Node Desc TOP: Regis Input Cont I ₁ : When _	The second in BCD for of (I_1) is us ription: ter to store	st word represe l word represe ormat. sed to determin the system tim) is presented	ents day i nts minute ne whethe ne.	n a week e; and the r this fund 15 1 Day	; the lo low by ction b 4 13 / in a w	w by yte o lock	yte c f the is to 11	f the sec	e first cond exec	st w l wo cute	ord 1 rd re d or <u>6 5</u> Hou	represen presen not.	nts t se 2 2 $23)_{\rm B}$		
high byte of numbers are Input contro Node Desc TOP: Regis Input Cont I ₁ : When the instruc	The second in BCD for of (I ₁) is us ription: ter to store ter to store ton is exect	st word represe l word represe ormat. sed to determin the system tim) is presented	ents day i nts minute ne whethe ne.	n a week e; and the r this fund 15 1 Day	; the lo low by ction b	w by yte o lock	yte c f the is to 11	f the sec	e first cond exec	st w l wo cute	ord 1 rd re d or <u>6 5</u> Hou	not.	nts t se 2 2 $23)_{\rm B}$		
high byte of numbers are Input contro Mode Descr TOP: Regis Input Cont I ₁ : When the instruc Function C	The second in BCD for of (I ₁) is us ription: ter to store ter to store ton is exect	st word represe l word represe ormat. sed to determin the system tim) is presented	ents day i nts minute ne whethe ne.	n a week e; and the r this fund 15 1 Day	; the lo low by ction b 4 13 / in a w	w by yte o lock	yte c f the is to 11	f the sec	e first cond exec	st w l wo cute	ord 1 rd re d or <u>6 5</u> Hou	represen presen not.	nts t se 2 2 $23)_{\rm B}$		
high byte of numbers are Input contro <u>Node Desc</u> TOP: Regis <u>Input Cont</u> I ₁ : When _	The second in BCD for of (I ₁) is us ription: ter to store ter to store ton is exect	st word represe l word represe ormat. sed to determin the system tim) is presented	ents day i nts minute ne whethe ne.	n a week e; and the r this fund 15 1Day	; the lo low by ction b 4 13 / in a w	w by yte o lock	yte c f the is to 11	f the sec	e first cond exec	st w l wo cute	ord 1 rd re d or <u>6 5</u> Hou	represen presen not.	nts t se 2 2 $23)_{\rm B}$		

(EXAMPLE)



[DESCRIPTION]

When contact 10001 is energized, the system time is copied to registers 40001 and 40002 in BCD format. Assume that the time reads:

 $(40001) = 0212_{BCD}$

 $(40002) = 2345_{BCD}$

Then the system time is Tuesday, 23 minutes 45 seconds past 12 o'clock.



Function outputs can be used to determine whether the function block has been executed.

Node Description:

TOP: Register to set the system time, 2 word format as follows :

Input Control:

I_1 :	When		L	is	presented,
---------	------	--	---	----	------------

the instruction is executed.

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
\mathbf{W}_1	Da	y in	a w	eek	(01~	-07)	BCD			Ho	our	(00)~2	23)	BCD		
W_2		Date(01~31) _{BCD}							Second (00~59) _{BCD}								

Function Output:

 $O_1 = I_1$

 $O_2 = 0$



[DESCRIPTION]

When contact 10001 is energized, the data stored in registers 40001 and 40002 are used to set the system time.

If $(40001) = 0212_{BCD}$

 $(40002) = 2345_{BCD}$

The setting date is Tuesday, 23 minutes and 45 seconds past 12 o'clock.



(EXAMPLE)



[DESCRIPTION]

When contact 10001 is energized, the reference time stored in registers 40001, and a constant 1110h is compared against the system time for any difference.

If the bottom node is represented by X_1 , X_2 , X_3 , X_4 (hex), then

X₁: nibble mask for the day in a WEEK

X₂: nibble mask for the HOUR

X₃: nibble mask for the MINUTE

X₄: nibble mask for the SECOND

Assume the system time is Tuesday, 25 minutes 34 seconds past 7 o'clock, and $40001=0208_{BCD}$ $40002=2536_{BCD}$ Bottom node =1110_H - (Second comparison is suppressed.)

Since the reference hour (8) is larger than the system hour (7), thus, coil 00001 is turned 'ON'.

Note: The date comparison is made according to the following order:

Week->Hour->Minute ->Second.

If any difference is found while comparing the higher ranked unit, the function output is set based on the comparison result, and the rest of the data is ignored.


PLC STATUS Description:

Word Order	De	Description								
000	R	eserved								
001	Second	Minute								
002	Hour	Day of a week								
003	Month	Date								
004	Year	Dummy byte								
005	Maximum sca	in time, unit: 100 us								
006	Minimum sca	n time, unit: 100 us								
007	Current scan	time, unit: 100 us								
008	PLC Elapsed time s	ince powered on (minute)								
009	PLC Elapsed time	since powered on(hour)								
010	Run-ti	me Status(1)								
011	Self-diag	nosis Status(2)								
012	PLC lir	ık - group ID								
013	PLC li	nk - link flag								
014	PLC link - rea	l time response state								
015	Drop	used state								

Detailed Description:

Word - 000: Reserved

Word-001 ~ Word - 004: System date and time.

Word-005: Maximum Scan Time, unit: 100 us

Word-006: Minimum Scan Time, unit: 100 us

Word-007: Current Scan Time, unit: 100 us

Word-008 : Elapse time since powered on (minute)

Word-009 : Elapse time since powered on (hour)

Word-010 ~ Word - 011: Run-time and Self-diagnosis:

a .Word-010: Self-diagnosis Status(1)

15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00

- bit00: RAM checksum error
- bit01: Real time timer error
- bit02: Watch dog timer error
- bit03: Status RAM fail
- bit04: Ladder RAM fail
- bit05: Remote I/O module fail
- bit06: Battery low
- bit07: Ladder error
- bit08: I/O map error
- bit09: reserve
- bit12: reserve
- bit13: reserve
- bit14: Local I/O module fail

b. Word - 011: Self-diagnosis Status(2)

_	15	14	13	12	11	10	09	08	07	00	05	01	00	02	01	00

bit00: Local I/O mismatch

bit01: Installed I/O points are over the system limitation

bit02: Remote I/O mismatch

bit04: Ladder syntax error

bit05: Rack I/O mismatched

bit06 ~ bit15: Reserved

Word-012 : Group ID (Hexadecimal) for PLC link

Word-013 : Link flag map of master station

Bit1 to Bit15 are corresponding to the slave station #1 to station #15.

bit = '1' : The corresponding slave station is required to be linked.

bit = '0' : The corresponding slave station is not required to be linked.

Word-014 : PLC link–linking status

Bit1 to Bit15 are corresponding to the slave station #1 to station #15

bit = '1' : Link normal, the corresponding station is required to be linked.

bit = '0' : Link abnormal, the corresponding station is not required to be linked.

Word-015 : Communication status of the remote drops

Bit1 to Bit 15 are corresponding to the drop #1 to drop #15.

Bit = '1' the corresponding drop has occurred communication error.

CHAPTER 6: OTHERS



This function is similar to a 16-contact stepping switch. The source register (defined in the TOP node) is compared against the table formed by 32 target registers (16 sets in total, defined in the middle node), and the corresponding bit is set to 'ON' in the bottom node.

Input control (I_1) is used to determine whether this function block is to be executed or not.

Remark: The restriction of the middle node is that the content of the first register must be hero than the content of the second register for each pair.

Node Description:

TOP: Target register

MIDDLE: 16 pairs of data, 32 registers.

BOTTOM: Comparison result. (Assume that the top node is represented by W_b and the middle node pair is represented by W₀, W₁.)

= 1: if $((W_b) \ge (W_0))$ and $((W_b) < (W_1))$

Input Control:

I₁: When \Box (\Box) is presented, the instruction is executed.

Function Output:

 $O_1 = I_1$

 $O_2 = 0$

O₃=0

(EXAMPLE)



[DESCRIPTION]

The top node of the CAM switch is coming from a T1.0 timer register. It cycles from $0\sim155$. The CAM table is defined in the middle node. It starts from 0 and has an increment of 5. After comparing the content of register 40006(for example 145) and the CAM table, this function sets the 14th bit to on and returns 16384 to the bottom node. After 5 seconds, the 15th bit would be "ON". After another 5 seconds, the 1st bit would be 'ON. This behavior is very similar to that of a CAM switch.





- O₃: Parameter status
 - =0, Parameter OK
 - =1, Parameter error.

TOP Node: Location of the intelligent module

The location of an intelligent module is defined via **Drop.Rack.Slot.** The range of Drop number is from 0 to 15. The range of Rack number is from 1 to 4. The Range of Slot number is from 1 to 8.

	b15 l	b14	b13	b12	b1	b10	b09	b08	b07	b06	b05	b04	b03	b02	b01	b00
word 1					Ir	ntellig	gent r	nodu	le ad	dress	offs	et				
word 2	Reserved									N	3		N2		N1	
word 3		Target address														

Middle Node: Common Data Memory

Word 1: Intelligent module CDM data address offset

Word 2: Reserved.

Word 3: Target data starting address of the register for store data after read from CDM. This address is mapping to 4xxxx registers. For example, if word3 = 00010, then the data is stored starting from register 40010.

[EXAMPLE]

To read the first 10 words in PAGE0 of the common data memory for an A/D module installed at Rack 2, Slot 4, and store the result in registers 40100~40109, set the middle node as follows: (40001)=0000, (40003)=0100.



Ladder program:



[DESCRIPTION]

Assume that (40001)=0000, and (40003)=100; when there is an OFF \Box ON transition for contact 10001, the CDMR function is executed. During execution, coil 00001 is 'ON'. After execution, coil 00001 = coil 00003 = 'OFF', and coil 00002 = 'ON'.



- =1, Finished
- =0, not finished yet.
- O₃: Parameter status
 - =0, Parameter OK
 - =1, Parameter error.

TOP Node: Location of the intelligent module

The location of an intelligent module is defined via **Drop.Rack.Slot.** The range of Drop number is from 0 to 15. The range of Rack number is from 1 to 4. The range of Slot number is 1 to 8.

	b15	b14	b13	b12	b1	b10	b09	b08	b07	b06	b05	b04	b03	b02	b01	b00
word 1	Intelligent module address offset															
word 2		Reserve									N	[3		N2		N1
word 3		Target address														

Middle Node: Common Data Memory

Word 1: Intelligent module address offset

Word 2: Reserved.

Word 3: Source data starting address of the register for copy data to CDM. This address is mapping to 4xxxx registers. For example, if word3 = 00010, then the data is transferred to the intelligent module starting from register 40010.

[EXAMPLE]

To read the 10 words data in registers 40100~40109 and write to PAGE0 of the common data memory for an A/D module installed at Rack 2, Slot 4, set the middle node as follows: (40001)=0000, (40003)=0100.



(DESCRIPTION)

Assume that (40001)=0000, and (40003)=100; when there is an OFF \Box ON transition for contact 10001, the CDMW function is executed. During execution, coil 00001 is 'ON'. After execution, coil 00001 = coil 00003 = 'OFF', and coil 00002 = 'ON'.

PID PID Control Symbol Image: Control image: C													PID
In Top On In Top In	PII	C		PID Cont	rol						_		
I1 Top O1 Middle O2 Middle O1 I3 Bottom O3 Bottom O1 Descriptions: Bottom O3 Bottom O1 This function block calculate the difference between the present value and the set-point, and control signal to minimize the difference via PID calculation. Nodes: Top PID function parameters. Please see the next page. Middle Widdle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. Intervent of the present value is obtained from manual input. Error detecting is still enabled. Status transition during Manual to Auto mode switching. = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. Status transition during Manual to Auto mode switching. = 1, Bumpless transition during Manual to Auto mode switching. = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. DUTEVET Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. D2: = 1, if there is any parameter error. D2: = 1, if the present value (scaled PV) is higher than the high alarm limit. D2: = 1, if the present value (scaled	<u>ymbol</u>		-	<u>Operands</u>				1		1	1	1	
Middle Niddle Niddle Niddle I_3 Bottom O ₃ Middle O I Bottom O ₃ Bottom O I I Descriptions: Bottom O I </th <th></th> <th></th> <th></th> <th></th> <th>0</th> <th>1</th> <th>3</th> <th></th> <th>C</th> <th>P</th> <th>L</th> <th></th> <th></th>					0	1	3		C	P	L		
I3 Bottom O Bottom O O Descriptions: This function block calculate the difference between the present value and the set-point, and control signal to minimize the difference via PID calculation. Nodes: Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. IMPUT: I3: Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I4: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. [5: Direct/Reverse Mode] = 1, Decrease Output as Error increases. OUTPUT: O: = 1, if there is any parameter error. O: = 1, if there is any parameter error. O: = 1, if the present value (scaled PV) is higher than the high alarm limit. O: = 1, if the present value (scaled PV) is lower than the low alarm limit.	\mathbf{I}_1	-	O ₁	Тор				0					
Descriptions: This function block calculate the difference between the present value and the set-point, and control signal to minimize the difference via PID calculation. Nodes: Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. NPUTE: a:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. a: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. a: Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. DI:= 1, if there is any parameter error. D2:= 1, if the present value (scaled PV) is higher than the high alarm limit. D3:= 1, if the present value (scaled PV) is lower than the low alarm limit.	I ₂	PID	$-O_2$	Middle				0					
This function block calculate the difference between the present value and the set-point, and control signal to minimize the difference via PID calculation. Nodes: Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. NPUT: A:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. 2: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. 3: Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. DUTPUT: D_1: = 1, if there is any parameter error. D_2: = 1, if the present value (scaled PV) is higher than the high alarm limit. D_3: = 1, if the present value (scaled PV) is lower than the low alarm limit.	I ₃ —	Bottom	— O ₃	Bottom					0				
This function block calculate the difference between the present value and the set-point, and control signal to minimize the difference via PID calculation. Nodes: Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. NPUT: 1:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. 2: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. 3 : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. DUTPUT: D_1: = 1, if there is any parameter error. D_2: = 1, if the present value (scaled PV) is higher than the high alarm limit. D_3: = 1, if the present value (scaled PV) is lower than the low alarm limit.													
This function block calculate the difference between the present value and the set-point, and control signal to minimize the difference via PID calculation. Nodes: Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. NPUT: In: In: In: In: In: In: In: In: In: In	escription	18.											
 control signal to minimize the difference via PID calculation. Nodes: Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. NPUT: a:Auto/Manual Mode a: 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. a: Bumpless transition during Manual to Auto mode switching. a: Direct/Reverse Mode b: Direct/Reverse Mode c: 1, Decrease Output as Error increases. DITE Direction: Direction: Direction:<!--</th--><th></th><th></th><th>1 1 . 4 . 4 . 4</th><th></th><th></th><th></th><th> 1</th><th></th><th>.1.41</th><th></th><th>4</th><th>·</th><th></th>			1 1 . 4 . 4 . 4				1		.1.41		4	·	
Nodes: Fop: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. NPUT: 1:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. 2: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. 3: Direct/Reverse Mode = 1, Decrease Output as Error increases. D1: = 1, if there is any parameter error. D2: = 1, if the present value (scaled PV) is higher than the high alarm limit. D3: = 1, if the present value (scaled PV) is lower than the low alarm limit.	I his func	tion block ca	alculate the d	ifference between the	e pres	sent	valu	ie ar	ia tr	ne se	t-po	int, and	produce
 Fop: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: (1:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. (2: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. (3: Direct/Reverse Mode = 1, Decrease Output as Error increases. DUTPUT: (2: 0, Increase Output as Error increases.) (3: 0, Increase Output as Error increases.) DUTPUT: (2: 1, if there is any parameter error.) (2: 2: 1, if the present value (scaled PV) is higher than the high alarm limit. (2: 2: 1, if the present value (scaled PV) is lower than the low alarm limit. 	control si	ignal to miniı	mize the diffe	erence via PID calcul	ation	•							
 Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: In: Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. Is Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. Is Direct/Reverse Mode = 1, Decrease Output as Error increases. OUTPUT: Output: Output: If there is any parameter error. O₂: = 1, if there resent value (scaled PV) is higher than the high alarm limit. O₃: = 1, if the present value (scaled PV) is lower than the low alarm limit. 													
 Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: I1:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I2: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I3: Direct/Reverse Mode = 1, Decrease Output as Error increases. OUTPUT: O₁:= 1, if there is any parameter error. O₂:= 1, if the present value (scaled PV) is higher than the high alarm limit. O₃:= 1, if the present value (scaled PV) is lower than the low alarm limit. 													
Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: I ₁ :Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I ₂ : Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I ₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O ₁ : = 1, if there is any parameter error. O ₂ : = 1, if the present value (scaled PV) is higher than the high alarm limit. O ₃ : = 1, if the present value (scaled PV) is lower than the low alarm limit.													
Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: I ₁ :Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I ₂ : Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I ₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O ₁ : = 1, if there is any parameter error. O ₂ : = 1, if the present value (scaled PV) is higher than the high alarm limit. O ₃ : = 1, if the present value (scaled PV) is lower than the low alarm limit.													
 Top: PID function parameters. Please see the next page. Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: I1:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I2: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I3: Direct/Reverse Mode = 1, Decrease Output as Error increases. OUTPUT: O₁:= 1, if there is any parameter error. O₂:= 1, if the present value (scaled PV) is higher than the high alarm limit. O₃:= 1, if the present value (scaled PV) is lower than the low alarm limit. 													
 Middle: Working Area and Status area for PID function. Please see the following page. Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: I1: Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I2: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I3: Direct/Reverse Mode = 1, Decrease Output as Error increases. OUTPUT: O₁: = 1, if there is any parameter error. O₂: = 1, if the present value (scaled PV) is higher than the high alarm limit. O₃: = 1, if the present value (scaled PV) is lower than the low alarm limit.	lodes:												
Bottom: Cycle time for PID Function, unit: 1/10 sec. INPUT: I ₁ :Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I ₂ : Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I ₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O ₁ : = 1, if there is any parameter error. O ₂ : = 1, if the present value (scaled PV) is higher than the high alarm limit. O ₃ : = 1, if the present value (scaled PV) is lower than the low alarm limit.	op: PID fu	inction paran	neters. Please	e see the next page.									
 INPUT: I1:Auto/Manual Mode a), Output is controlled by PID function, b), Output is obtained from manual input. c), Error detecting is still enabled. I2: Bumpless transition during Manual to Auto mode switching. a), Bumpless transition enabled, b), Bumpless transition disabled. I3: Direct/Reverse Mode b), Directase Output as Error increases. c), Increase Output as Error increases. OUTPUT: O1: a), if there is any parameter error. O2: a), if the present value (scaled PV) is higher than the high alarm limit. O3: a), if the present value (scaled PV) is lower than the low alarm limit. 	liddle: Wo	rking Area a	nd Status are	ea for PID function.	Please	e see	e the	foll	owi	ng p	age.		
 I₁:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I₂: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O₁: = 1, if there is any parameter error. O₂: = 1, if the present value (scaled PV) is higher than the high alarm limit. O₃: = 1, if the present value (scaled PV) is lower than the low alarm limit. 	Sottom: Cy	cle time for l	PID Function	n, unit: 1/10 sec.									
 I₁:Auto/Manual Mode = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I₂: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O₁: = 1, if there is any parameter error. O₂: = 1, if the present value (scaled PV) is higher than the high alarm limit. O₃: = 1, if the present value (scaled PV) is lower than the low alarm limit. 													
 = 1, Output is controlled by PID function, = 0, Output is obtained from manual input. Error detecting is still enabled. I₂: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O₁: = 1, if there is any parameter error. O₂: = 1, if the present value (scaled PV) is higher than the high alarm limit. O₃: = 1, if the present value (scaled PV) is lower than the low alarm limit. 	NPUT:												
Error detecting is still enabled. I ₂ : Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I ₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O ₁ : = 1, if there is any parameter error. O ₂ : = 1, if the present value (scaled PV) is higher than the high alarm limit. O ₃ : = 1, if the present value (scaled PV) is lower than the low alarm limit.	:Auto/Mai	nual Mode											
 I₂: Bumpless transition during Manual to Auto mode switching. = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I₃: Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O ₁ : = 1, if there is any parameter error. O ₂ : = 1, if the present value (scaled PV) is higher than the high alarm limit. O ₃ : = 1, if the present value (scaled PV) is lower than the low alarm limit.	= 1, Outp	out is control	led by PID ft	unction, $= 0$, Output	t is o	btai	ned	fron	n ma	anua	l inp	ut.	
 = 1, Bumpless transition enabled, = 0, Bumpless transition disabled. I₃ : Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O ₁ : = 1, if there is any parameter error. O ₂ : = 1, if the present value (scaled PV) is higher than the high alarm limit. O ₃ : = 1, if the present value (scaled PV) is lower than the low alarm limit.				Erro	r dete	ectin	ıg is	still	ena	bled	•		
 I₃: Direct/Reverse Mode = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O₁: = 1, if there is any parameter error. O₂: = 1, if the present value (scaled PV) is higher than the high alarm limit. O₃: = 1, if the present value (scaled PV) is lower than the low alarm limit. 	2: Bumples	s transition d	luring Manua	al to Auto mode swit	ching	•							
 = 1, Decrease Output as Error increases. = 0, Increase Output as Error increases. OUTPUT: O₁: = 1, if there is any parameter error. O₂: = 1, if the present value (scaled PV) is higher than the high alarm limit. O₃: = 1, if the present value (scaled PV) is lower than the low alarm limit. 	= 1, Bum	pless transiti	on enabled, =	= 0, Bumpless transit	ion d	isabl	led.						
OUTPUT: O_1 : = 1, if there is any parameter error. O_2 : = 1, if the present value (scaled PV) is higher than the high alarm limit. O_3 : = 1, if the present value (scaled PV) is lower than the low alarm limit.	3: Direct/R	Reverse Mode	e										
O_1 : = 1, if there is any parameter error. O_2 : = 1, if the present value (scaled PV) is higher than the high alarm limit. O_3 : = 1, if the present value (scaled PV) is lower than the low alarm limit.	= 1, Decr	ease Output	as Error incr	reases. $= 0$, Increa	se Oi	utpu	ıt as	Erro	or in	crea	ises.		
O_2 : = 1, if the present value (scaled PV) is higher than the high alarm limit. O_3 : = 1, if the present value (scaled PV) is lower than the low alarm limit.	<u>DUTPUT:</u>												
O_3 : = 1, if the present value (scaled PV) is lower than the low alarm limit.	$\mathbf{D}_1 := 1$, if the set of the	here is any pa	arameter erro	or.									
O_3 : = 1, if the present value (scaled PV) is lower than the low alarm limit.	$D_2: = 1$, if the set of the se	he present va	lue (scaled P	V) is higher than the	high	alar	m li	mit.					
		-		-	-								
185		UII.											

PID Control Loop:



PID formula:

$$MV(\mathbf{t}) = \frac{100}{P_b} \left[e(\mathbf{t}) + K_I \int e(\mathbf{t}) d\mathbf{t} + K_b \left(\frac{de(\mathbf{t})}{dt} \right) \right] + Bias$$

Where:

MV(t) = Control Output

Pb = Proportional Band

e(t)= Error (Difference between Scaled PV and SP)

 K_I = Constant for Integration Term, or, reset time constant

 K_D = Constant for Derivative Term, or, rate time constant

Bias = Correction Value, or offset to Output

TOP Node: Register: 4xxx1 ~ 4xx16

4xxx1: An internal register used to store the scaled PV in Engineering Unit.

Scaled $PV = \frac{Raw PV}{Sensor Range} * (UER - LER) + LER$

Where: Raw PV: Obtained from the difference of Register 4xx14 and 4xx16. UER : Upper bound of Engineering measurement Range (See also Register 4xx12)

LER : Lower bound of Engineering measurement Range (See also Register 4xx13)

Sensor Range: **4096**. Assuming that an AD020 module is used to convert Raw PV signal (0~10V) to digital data (0~65535), then the Raw PV must be divided by **16** first to maintain consistency.

4xxx2: Set Point in Engineering Unit. (0~9999)

- 4xxx3: PID control output MV (0~4096). Please use a proper scaling factor to scale this control output and then send to the Output Device.In Auto Mode (I₁ =1), the data in this register is the result of PID calculation. In Manual Mode (I₁ =0), filling this register by user is required.
- **4xxx4**: High alarm limit in Engineering Unit (0~9999). This number should be greater than the Set Point.
- 4xxx5: Low alarm limit in Engineering Unit (0~9999). This number should be less than the Set Point.
- **4xxx6**: Proportional Band (Pb:5~500). The term Proportional Band is also referred to as the "sensitivity". The reciprocal of Pb is "Gain". As seen from the PID formula, the "Gain" is the proportional factor between "Error" and output MV. For example: if Pb=5, then MV is amplified 20 times.
- **4xxx7**: Constant for Integration Term, or, Reset time Constant (K_I : 0~9999). As seen from the PID formula, the K_I represents the contribution of the Integral. If $K_I = 0$, then this function block becomes a PD function block.
- **4xxx8**: Constant for Derivative Term, or, Rate time Constant (K_D : 0~9999). As seen from the PID formula, the K_D represents the contribution of the Derivative. If $K_D = 0$, then this function block becomes a PI function block. If both $K_I = 0$ and $K_D = 0$, then this function block becomes a proportional control function block.
- 4xxx9: Bias, Correction Value, or offset to Output (0~4095).
- 4xx10: High integral wind-up limit, or, upper bound of output. Usually this value is set at 4095.
- 4xx11: Low integral wind-up limit, or, lower bound of output. Usually this value is set at 0000.
- **4xx12**: Upper bound of Engineering Range (0001~9999). Specify the upper limit of the sensor output in Engineering Unit in this register. For example, a RTD10 module produce unsigned digital data 1500 ~7500 for temperature 0°C ~600°C, then specify 600 for this register. This number should

be greater than the Set Point.

- 4xx13: Lower bound of Engineering Range (0000~9998). Specify the lower limit of the sensor output in Engineering Unit in this register. For example, a RTD10 module produce unsigned digital data 1500 ~7500 for temperature 0°C ~600°C, then specify 0000 for this register. This number should be less than the Set Point.
- 4xx14: Raw PV. Move the data from the output PV sensor to this register. (See also Register 4xx16)
- **4xx15**: Internal Register for storing the status of "Auto" or "Manual" mode. If the content of this register is 11(Hex), the PID function block is in Manual mode. If 55(Hex), Auto mode.
- **4xx16**: Correction value for Row PV. (0~4096). Specify a correction value in this register. This value is subtracted from the Raw PV (obtained from Register 4xx14), and the result is then used in the calculation of Register 4xx1.

Middle Node: Register 4yyy1~4yyyy5

4yyy1: PID function Block Status.

Bit 1: =1, if there is any parameter error. Bit 2: =1, if High Alarm limit is exceeded. Bit 3: =1, if Low Alarm limit is exceeded. Bit 4 ~ Bit 5: Reserved. Bit 6: =1, if PID function Block is in "Auto" mode and computing. Bit 7 ~ Bit 12: Reserved. Bit 13: =I₃ Bit 14: =I₂ Bit 15: =I₁ Bit 15: =I₁ Bit 16: Reserved.

4yyy2: Internal Register for PID Loop timer.

4yyy3: Internal Register for storing High order integral summation.

4yyy4: Internal Register for storing Low order integral summation.

4yyy5: Internal Register for storing Scaled PV used in the previous scan.

Bottom Node: Cycle time, unit: 1/10 sec. 00010 stands for one second.

Example:





40002: 1770(Hex)=6000 (=7500-1500), Data range for 0~600°C 40003: Quotient

40001: Obtain from RTD module

40004: 1000(Hex)=4096, Sensor Range

40005: Product, Raw PV

Move Raw PV to PID's TOP node Register 41014.

41001

41002: 0220°C, processing temperature for HDPE 41003: Control Output (MV) 41004: 0250°C, High Alarm Limit 41005: 0200°C, Low Alarm Limit 41006: 0200, Proportional Band 41007: 0300, KI 41008: 0000, thus, PI control only. 41009: 0000, Bias 41010: 4095, upper bound of MV 41011: 0000, lower bound of MV 41012: 0600°C, UER 41013: 0000°C, LER 41014: Raw PV 41015: 0055, Auto mode 41016: 0000, Correction for Raw PV Bottom Node: Cycle time, 2 sec. Move PID output to DA020 module